

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yujiro KAJIHARA et al.

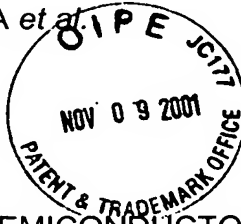
Serial No.: 09/328,910

Filed: 9 June 1999

For: LEAD FRAME SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE, USING THE SAME, AND METHOD  
OF AND PROCESS FOR FABRICATING THE TWO

Art Unit: 2851

Examiner: J. Clark



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PETITION FOR EXTENSION OF TIME

Honorable Commissioner for Patents  
Washington, D.C. 20231

9 November 2001

Sir:

Applicant respectfully petitions the Commissioner under 37 CFR §1.136 for an extension of the period for response to the restarted Office Action mailed 11 June 2001 in connection with the above-identified application.

Attached hereto is a Form PTO-2038, authorizing the appropriate fee for this Petition. To whatever other extent is necessary, Applicant petitions for an appropriate extension of time. Please charge any fees necessitated by this filing to ATS&K Deposit Account No. 01-2135 (as Order No. 501.32049R00), and credit any overpayment or excess fee thereto.

Respectfully submitted,

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